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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/600,562	06/20/2003	Jaroslav Hynecek	TI-36361	6063	
7590 06/14/2005			EXAM	EXAMINER	
Alan K. Stewart			SOHN, SEUNG C		
Texas Instrumer	nts Incorporated				
P.O. Box 655474, MS 3999			ART UNIT	PAPER NUMBER	
Dallas, TX 75	Dallas, TX 75265			2878	
			DATE MAILED: 06/14/2005	5	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/600,562	HYNECEK, JAROSLAV				
Office Action Summary	Examiner	Art Unit				
	Seung C. Sohn	2878				
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailir earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONED	nely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on	Responsive to communication(s) filed on					
<i>i</i> —	This action is FINAL. 2b)⊠ This action is non-final.					
	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ☐ Claim(s) 1-8 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-8 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examin	er.					
10)⊠ The drawing(s) filed on is/are: a)□ accepted or b)⊠ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) A) Interview Summary (PTO-413) Paper No(s)/Mail Date						
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date <u>903</u>. 		atent Application (PTO-152)				

DETAILED ACTION

Drawings

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the transistor for the voltage reference generator in claim 7 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

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Claim Rejections - 35 USC § 112

2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

3. Claims 1-8 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 1 recites the limitation "the optical diode" in line 10. There is insufficient antecedent basis for this limitation in the claim. Claim 4 recites the limitation "the floating diffusion region" in line 2. There is insufficient antecedent basis for this limitation in the claim. Claim 7 recites the limitation "the resetshielding transistor" in line 7. There is insufficient antecedent basis for this limitation in the claim.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Hynecek (Patent Pub. App. No. US 2002/0017646).

Regarding claims 1-2, 5 and 7, Hynecek shows in Fig. 2 a charge harge detection device comprising a floating diffusion (209); a feed through shielding transistor

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(208) coupled to the floating diffusion; a reset transistor (208) coupled to the shielding transistor; an output diode diffusion (210) coupled to the reset transistor; a bias tracking voltage reference generator (Vog) coupled to the output diode for providing bias to the output diode; and wherein an input of the reference generator is coupled to a gate of the feed through shielding transistor (Paragraphs 0019 & 0021).

Regarding claim 3, Hynecek discloses that a fixed amount of charge is kept under the gate of the feed through shielding transistor to provide a reset time constant (see abstract).

Regarding claims 4 and 6, Hynecek discloses that a low-doped region surrounds the floating diffusion region and is adjacent to the gate of the feed through shielding transistor for the purpose of minimizing the gate to n+ overlap capacitance (214) (Paragraph 0017).

Regarding claim 8, Hynecek discloses that a predetermined amount of charge is maintained in a channel of the reset-shielding transistor after a reset has been made, independent of process parameter variations and gate bias variations (Paragraph 0021).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Seung C. Sohn whose telephone number is (571) 272-2446. The examiner can normally be reached on Monday through Friday from 8:30 am to 5 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Porta can be reached on (571) 272-2444. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Seung C. Sohn Examiner Art Unit 2878

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